



501.39932X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NOGUCHI et al  
Serial No.: 09/825,946  
Filed: April 5, 2001  
For: Semiconductor Integrated Circuit Device And  
Fabrication Method For Semiconductor  
Integrated Circuit Device  
Group: 2811  
Examiner: H. Vu

SUBMISSION OF VERIFIED ENGLISH TRANSLATION  
OF PRIORITY DOCUMENT

Assistant Commissioner for Patents  
Washington, DC 20231

August 28, 2002

Sir:

Applicants are submitting herewith a verified English translation of applicants'  
priority document, JP 2000-104015, filed April 5, 2000.

Respectfully submitted,

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Attachment